



ISCAS 2015

INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS
24-27 May 2015, Lisbon, Portugal

ISCAS 2015 Organizing Committee

Honorary Chair

Manuel Medeiros-Silva, INESC-ID/IST, U. Lisboa, Portugal

General Co-Chairs

Jorge Fernandes, INESC-ID/IST, U. Lisboa, Portugal
Wouter Serdijn, TU Delft, The Netherlands

Technical Program Committee Co-Chairs

João Goes, FCT/UNL and CTS/UNINOVA, Portugal
Philipp Haffliger, Univ. of Oslo, Norway

Special Sessions Co-Chairs

Nuno Paulino, FCT/UNL and CTS/UNINOVA, Portugal
Pui-In Mak, Univ. Macau, China

Tutorial Co-Chairs

José De la Rosa, Inst. Microelectrónica de Sevilla, Spain
Yehia Massoud, Rice University, USA

Finance Co-Chairs

Luis Miguel Silveira, INESC-ID/IST, U Lisboa, Portugal
Paulo Flores, INESC-ID/IST, U Lisboa, Portugal

Local Arrangement Co-Chairs:

Luis Oliveira, FCT/UNL and CTS/UNINOVA, Portugal
António Couto-Pinto, INESC-ID/ISEL, Portugal
Pedro Santos, IT/ Acad. Militar, Portugal
Carlos Beltran, INESC-ID, Portugal
Rui Santos-Tavares, FCT/UNL & CTS/UNINOVA, Portugal
Helena Sarmento, INESC-ID/IST, U Lisboa, Portugal

Publicity Co-Chairs:

Rui Martins, Univ. Macau, China / IST, U Lisboa, Portugal
Ricardo Reis, UFRGS, Brazil
Amara Amara, ISEP, France

Publication Co-Chairs

Teresa Almeida, INESC-ID/IST, U Lisboa, Portugal
Nuno Borges Carvalho, IT/Univ. Aveiro, Portugal

Industry & Exhibition Co-Chairs:

Angel Rodriguez-Vázquez, Anafocus, Spain.
Pedro Figueiredo, Synopsys, Portugal.
Marcelino Bicho dos Santos, Silicon-Gate, Portugal.
Noel O'Riordan, S3group, Ireland.
Patrick Quinn, Xilinx Inc., Ireland.
John Blake, ON Semiconductors, Ireland.

Demo Sessions Co-Chairs:

Pantelis Georgiu, Imperial College London, UK
João Pedro Oliveira, FCT/UNL & CTS/UNINOVA, Portugal
Robert Rieger, National Sun Yat-sen University, Taiwan

PhD/GOLD Special Session Co-Chairs

Martin di Federico, Univer. Nacional del Sur, Argentina
Nuno Horta, IT/IST, U Lisboa, Portugal

Women in CASS Chair:

Yoko Uwate, Tokushima University, Japan
Pamela Abshire, University of Maryland, USA

Web Co-Chairs:

Nuno Roma, INESC-ID/IST, U Lisboa, Portugal
Pedro Tomás, INESC-ID/IST, U Lisboa, Portugal

IEEE CAS Society Executive Office:

Erin Bise, Conference Catalysts, LLC, USA

Conference Organization:

Barbara Wehner, AMG, USA

2015 IEEE International Symposium on Circuits and Systems

The IEEE International Symposium on Circuits and Systems (ISCAS) is the world's premier networking forum for leading researchers in the highly active fields of theory, design and implementation of circuits and systems. ISCAS 2015, sponsored by the IEEE Circuits and Systems Society and supported by Instituto Superior Técnico and INESC-ID of Universidade de Lisboa and by the Faculty of Sciences and Technology and CTS/UNINOVA of Universidade Nova de Lisboa, will be held in Lisbon, Portugal, from May 24 to 27, 2015. ISCAS 2015 will have a special focus on Enabling Technologies for Societal Challenges, and seeks to address multidisciplinary challenges in circuits and systems using microelectronics, nanotechnologies, biotechnologies and signal processing to address health, demographic change, wellbeing, security and sustainability issues. The symposium will include oral and poster sessions, live demonstrations, tutorials given by experts on state-of-the-art topics, and special sessions, with the aim of complementing the regular program with emerging topics of particular interest to the circuits and systems community.

Prospective authors are invited to submit papers including technical novelties and tutorial overviews in the areas listed below, or in other areas relevant to the ISCAS 2015 special theme.

- Analog Signal Processing
- Biomedical and Life Science Circuits and Systems
- Cellular Neural Networks and Array Computing
- Circuits and Systems for Communications
- Computer Aided Network Design
- Digital Signal Processing
- Education in Circuits and Systems
- Live Demonstrations of Circuits and Systems
- Multimedia Systems and Applications
- Nanoelectronics and Gigascale Systems
- Neural Systems and Applications
- Nonlinear Circuits and Systems
- Power and Energy Circuits and Systems
- Sensory Systems
- Visual Signal Processing and Communications
- VLSI Systems and Applications

Potential authors for regular papers, live demos, tutorials, and special sessions, please visit the conference website <http://www.iscas2015.org> and click the corresponding icons for details. For regular sessions, authors are invited to submit 4-page full papers according to the posted guidelines. Only electronic submissions will be accepted via the Web at: <http://www.epapers.org/iscas2015>. Authors of accepted papers are expected to present their papers at the Symposium and at least one author of each paper MUST register for the Symposium in order for the papers to be included in the program.

Important Dates:

Deadline for submission of Tutorials Proposals:	19 September 2014
Deadline for submission of Special Session Proposals:	19 September 2014
Notification of acceptance of Special Session Proposals:	3 October 2014
Submission of full four-page papers for Regular Sessions:	10 October 2014
Submission of 1-pagers for live demonstrations:	10 October 2014
Submission of full four-page papers for Special Sessions:	31 October 2014
Notification of acceptance of Tutorial Proposals:	31 October 2014
Notification of paper acceptance:	8 January 2015
Deadline for submission of final papers:	6 February 2015
Deadline for author registration:	6 February 2015

Call for papers in: <http://www.iscas2015.org/CfP.pdf>

